

## ANNEXURE 5.2

### 5.2 Innovations by the Faculty in Teaching and Learning

Following are the innovative techniques incorporated by the faculty in the teaching and learning process:

Sl. No.	Name of the Faculty	Subject	Innovation incorporated in Teaching and Learning
1.	Dr. K. Shridhar	Digital Signal Processing	Demonstration of complex DSP concepts through simulation using MATLAB.
2.	Dr. K. Shridhar	Digital Signal Processing	Interactive contents on topic Fast Fourier Transform. <a href="https://docs.google.com/presentation/d/1WfYMCg7GBVueFVwJVROS1vEqEAueqKjc/edit?usp=drive_link&amp;ouid=101099264730601342587&amp;rtpof=true&amp;sd=true">https://docs.google.com/presentation/d/1WfYMCg7GBVueFVwJVROS1vEqEAueqKjc/edit?usp=drive_link&amp;ouid=101099264730601342587&amp;rtpof=true&amp;sd=true</a>
3.	Dr. Jayashree D. Mallapur	Multimedia Communication	Assignments for the subject multimedia communication handled by the faculty. <a href="https://classroom.google.com/c/NjQyODQ4Nzg1NzUy/p/NjUwNTIyNjQxNTk3/details">https://classroom.google.com/c/NjQyODQ4Nzg1NzUy/p/NjUwNTIyNjQxNTk3/details</a>
4.	Dr. Nagarathna Rajur	Internet of Things	Testing models knowledge. <a href="https://forms.gle/qGRRW723Q7S3JAW76">https://forms.gle/qGRRW723Q7S3JAW76</a>
5.	Dr. Vijaylakshmi Jigajinni	Image Processing	Self evaluation by students after their presentation: as part of assignment A rubrics is prepared for evaluating the student performance after their presentations and they are made to fill the goggle forms by themselves so that they can find out by themselves how they presented the assignment.
6.	Dr. Ashok Sutagundar	Computer Networks	Power point presentations, Demonstrations of network equipments in the classes
7.	Dr. S. G. Kambalimath	Verilog Programming	Website is used for teaching Verilog Programming as additional material. <a href="http://www.chipverify.com/">http://www.chipverify.com/</a>
8.	Dr. S. G. Kambalimath	UEC543C: Verilog Programming	Project-Based Learning (PBL)
9.	Dr. S. G. Kambalimath	Digital System Design using Verilog	Simulations
10.	Dr. S. G. Kambalimath	UEC543C: Verilog Programming	Self-Paced Learning
11.	Dr. Vijaylakshmi S. Jigajinni	Aircraft electronics system	TEACHER FEEDBACK ON CURRICULUM AND INFRASTRUCTURE 2023-24 <a href="https://docs.google.com/forms/d/1f1o7tGDmVF8Y9-VN2eKsePlqa6gvPqRLrkFVM2XQZ5A/viewform?edit_requested=true">https://docs.google.com/forms/d/1f1o7tGDmVF8Y9-VN2eKsePlqa6gvPqRLrkFVM2XQZ5A/viewform?edit_requested=true</a>
12.	Dr. Vijaylakshmi S. Jigajinni	Aircraft electronics and system	Presentation by group of students on topic given. The evaluation of the presentation was done using

			the Google spread sheets. Link for the same is as given below. <a href="https://docs.google.com/spreadsheets/d/1picFe7E9s3TGEiK_fUCeeYlm31PJFr9czwkxwtmcxM/edit?usp=drivesdk">https://docs.google.com/spreadsheets/d/1picFe7E9s3TGEiK_fUCeeYlm31PJFr9czwkxwtmcxM/edit?usp=drivesdk</a>
13.	Dr. Vijavlakshmi S. Jigajinni	Aircraft electronics and system	Smart board utilization
14.	Mamata J Sataraddi	Network Analysis	Practical are done to clarify the theory concepts
15.	Dr. Ashok Sutagundar	Data Structure using C	Power point presentations, Simulation of the programs through online classes
16.	Dr. Ashok Sutagundar	Mobile Communications	Power point presentations, Demonstrations of network equipments in the classes

  
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## Fast Fourier Transform (FFT) Algorithms

Dr. K. Shridhar

## Relation to the z-transform

$$x(n) = \begin{cases} \text{Non zero,} & 0 \leq n \leq N-1 \\ 0, & \text{elsewhere} \end{cases} \quad X(z) = \sum_{n=0}^{N-1} x(n)z^{-n}$$

$$x(n) = \begin{cases} \tilde{x}(n), & 0 \leq n \leq N-1 \\ 0, & \text{elsewhere} \end{cases} \quad \tilde{X}(k) = \sum_{n=0}^{N-1} x(n) \left[ e^{j\frac{2\pi}{N}nk} \right]^n$$

$$\tilde{X}(k) = X(z) \Big|_{z=e^{j\frac{2\pi}{N}k}}$$

The DFT,  $X(k)$  represents  $N$  equally spaced samples of  $z$ -transform  $X(z)$ , on the circumference of unit circle.

## Relation to the DTFT

$$X(e^{j\omega}) = \sum_{n=0}^{N-1} x(n)e^{-j\omega n} = \sum_{n=0}^{N-1} \tilde{x}(n)e^{-j\omega n}$$

$$\tilde{X}(k) = X(e^{j\omega}) \Big|_{\omega=\frac{2\pi}{N}k}$$

$$\text{Let } \omega_1 = \frac{2\pi}{N}, \text{ and } \omega_k = \frac{2\pi}{N}k = k\omega_1$$

$$X(k) = X(e^{j\omega_k}) = X(e^{jk\omega_1})$$

DFT is obtained by uniformly sampling the DTFT at  $\omega_1$  intervals

The interval  $\omega_1$  is the *sampling interval* in the frequency domain. It is called *frequency resolution* because it tells us the minimum frequency quantum of information which we can have about the signal. Smaller the value better the resolution.

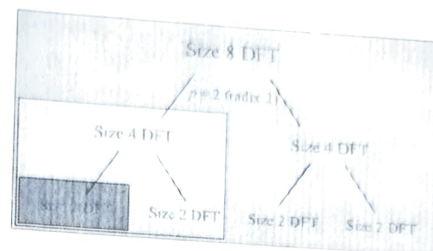
## Comments

- When we sample  $X(z)$  on the circumference of unit circle, we obtain a periodic sequence in the time domain.
- This sequence is a linear combination of the original  $x(n)$  and its infinite replicas, each shifted by multiples of  $N$  or  $-N$  samples.
- If  $x(n)=0$  for  $n < 0$  and  $n \geq N$ , then there will be no overlap or aliasing in the time domain.

## Important Points

- Zero-padding is an operation in which extra zeros are appended to the original sequence at the end of the signal. The resulting longer DFT provides closely spaced samples of the discrete Fourier transform of the original sequence.
- The zero-padding gives us a high-density spectrum and provides a better display of the spectrum. But it does not provide high-resolution spectrum because no new information is added to the signal; only additional zeros are inserted in the data.
- To get high-resolution spectrum, we need more data.
- More the data, more the information.

## Divide & Conquer Method



## Important Computational Requirements

- Total number of computations should be linear function rather than quadratic function of  $N$ .
- Most of the computations can be eliminated using the symmetry and periodicity properties of Twiddle Factors

$$W_N^{kn} = W_N^{k(n+N)} = W_N^{(k+N)n}$$

$$W_N^{kn+N/2} = -W_N^{kn}$$

Decimation-in-time FFT Algorithm: DIT-FFT Algorithm

Decimation-in-frequency FFT Algorithm: DIF-FFT Algorithm

## Radix-2 FFT Algorithms (DIT-FFT Algorithm)

- Let  $N=2^v$ ; then we choose  $M=2$  and  $L=N/2$  and divide  $x(n)$  into two  $N/2$ -point sequence.
- Above step is repeated. At each stage the sequences are decimated and the smaller DFTs are combined. This decimation ends after  $v$  stages.
- The resulting procedure is called Decimation-in-Time FFT (DIT-FFT) algorithm, for which the total number of complex multiplications is:  $C_N = Nv = N \log_2 N$ .
- Using additional symmetries:  $C_N = Nv = N/2 \log_2 N$



## Radix-2 FFT Algorithms (DIF-FFT Algorithm)

- Choose  $L=2$ ,  $M=N/2$  and follow the steps of DIT-FFT algorithm.
- Its signal flow graph is transposed structure of the DIT-FFT structure.
- Its computational complexity is also equal to  $C_N = N \log_2 N$

## Merits of Radix-2 FFT Algorithms

- To reduce computational complexity and increase computational efficiency, it is necessary to decompose  $N$  point DFT computation into successively smaller ( $N/2$ ,  $N/4$ ,  $N/8$ ,  $N/16$ , ..... ) DFT computations. In this process we have to exploit both symmetry and periodicity property of complex exponentials

$$W_N^{kn} = W_N^{k(n+N)} = W_N^{(k+N)n}$$

$$W_N^{kn+N/2} = -W_N^{kn}$$

## Decimation-in-time FFT Algorithm

- The principle of decimation-in-time is most conveniently illustrated by considering the special case of Let  $N = \text{An integer power of } 2$ ;  $N=2^v$
- Since  $N$  is an even integer, we can consider computing  $X(k)$  by separating  $x(n)$  into two  $N/2$ -point sequences consisting of the even-indexed points and odd-indexed points in  $x(n)$ . Mathematically  $X(k)$  is given by

$$X(k) = \sum_{n=0}^{N-1} x(n) W_N^{kn}, \quad k = 0, 1, \dots, N-1 \quad (2.3)$$

- Separating  $x(n)$  into even-numbered and odd-numbered points, we get new expression for  $X(k)$

$$X(k) = \sum_{\text{even}} x(n) W_N^{kn} + \sum_{\text{odd}} x(n) W_N^{kn}$$

Now substitute  $n=2r$  for an even and  $n=2r+1$  for odd indexed samples

$$X(k) = \sum_{r=0}^{N/2-1} x(2r) W_N^{k(2r)} + \sum_{r=0}^{N/2-1} x(2r+1) W_N^{k(2r+1)}$$

$$= \sum_{r=0}^{N/2-1} x(2r) (W_N^k)^r + W_N^k \sum_{r=0}^{N/2-1} x(2r+1) (W_N^k)^r \quad (2.4)$$

But we know that  $W_N^2 = W_{N/2}$

As a result previous equation (2.4) can be written as

$$X(k) = \sum_{n=0}^{N/2-1} x(2n)W_N^{kn} + W_N^k \sum_{n=0}^{N/2-1} x(2n+1)W_N^{kn} \quad k=0,1,\dots,N-1 \quad (2.5)$$

Each of the sums in equation (2.5) is recognized as an  $N/2$ -point DFT. Each of sums need only be computed for  $k = 0$  to  $N/2$  to give  $G(k)$  and  $H(k)$ . Since each  $G(k)$  and  $H(k)$  are periodic in  $k$  with period  $N/2$  further they can be decomposed into two  $N/4$ -point DFTs. This process should be continued till we reach at two point DFTs.

The computational flow or the signal flow in computing  $X(k)$  according to Eq. (2.5) for an 8-point sequence, i.e.  $N=8$  is shown in Figure below.

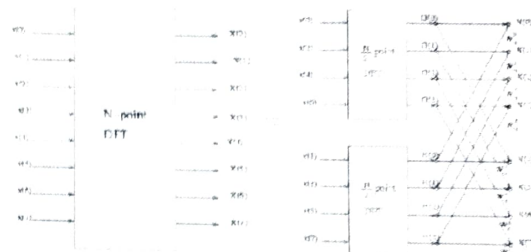


Figure 2.1: Flow graph of the decimation-in-time decomposition of an  $N$  ( $N=8$ ) point DFT into  $N/2$ -point DFT

- Further  $G(k)$  and  $H(k)$  in equation (2.5) can be computed as indicated in the next slide

$$\begin{aligned} G(k) &= \sum_{n=0}^{N/2-1} x(2n)W_N^{kn} \\ &= \sum_{n=0}^{N/4-1} x(4n)W_N^{2kn} + W_N^{2k} \sum_{n=0}^{N/4-1} x(4n+2)W_N^{2kn} \\ &= \sum_{n=0}^{N/4-1} x(4n)W_{N/2}^{kn} + W_N^{2k} \sum_{n=0}^{N/4-1} x(4n+2)W_{N/2}^{kn} \end{aligned} \quad (2.6)$$

On the same lines

$$H(k) = \sum_{n=0}^{N/2-1} x(2n+1)W_N^{k(2n+1)} + W_N^k \sum_{n=0}^{N/2-1} x(2n+1)W_N^{k(2n+1)} \quad (2.7)$$

Where  $g(r)=x(2r)$  and  $h(r)=x(2r+1)$

If the 4-point DFTs in Figure (2.1) are computed according to equations (2.6) and (2.7), then those computations would be carried out as indicated in Figure (2.2) drawn below.

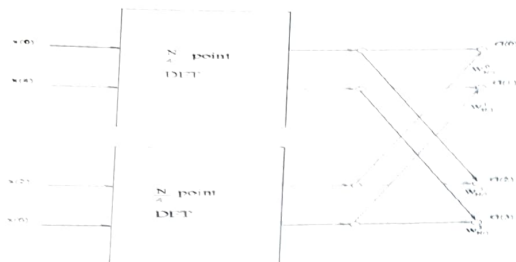


Figure 2.2: Flow graph of decimation-in-time decomposition of an  $\frac{N}{2}$  point DFT into  $\frac{N}{4}$  point DFT ( $N=8$ ).

Inserting the computation indicated in Figure (2.2) into the flow graph of Figure (2.1), we obtain the complete flow graph of Figure

(2.3). We have used the fact that  $W_{N/2} = (W_N)^2$ .

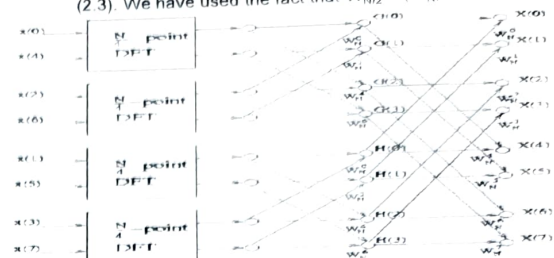


Figure 2.3: Result of substitution of flow graph of Figure (2.1) into flow graph of Figure (2.2).

## In-place Computations

- In view of Figure (2.4), the Figure (2.3) gives the complete computational flow graph for the  $N$ -point computation of DFT of  $N$ -point sequence, for  $N=8$ .
- An interesting by-product of this derivation is that, this flow graph, in addition to describing efficient procedure for computing the DFT, also suggests a useful way of storing the original data and storing the results of the computation in the intermediate arrays.

For  $N=8$ ,  $N/4$ -point DFT becomes 2-point DFT. The 2-point DFT of, for example,  $x(0)$  and  $x(4)$  is depicted in

Figure (2.4).

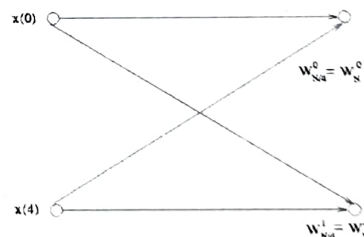


Figure 2.4: Flow graph of a two-point DFT (for  $N=8$ ).

We shall denote the sequence of complex numbers resulting from the  $m$ th stage of computation as  $X_m(l)$ , where  $l=0,1,\dots,N-1$ , and  $m=1,2,\dots$ , forming an input to the  $(m+1)$ st stage and producing an output  $X_{m+1}(l)$  as the output from the  $(m+1)$ st stage of computations. It can be seen that the basic computation in flow graph of Figure

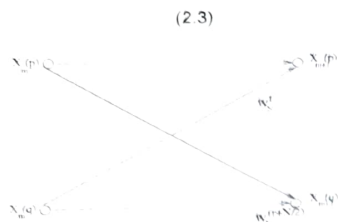


Figure 2.5: Flow graph of basic butterfly computation in Figure (2.3)

The equations represented by this flow graph are

$$X_{m+1}(p) = X_m(p) + W_N^p X_m(q)$$

$$X_{m+1}(q) = X_m(p) + W_N^{p+q} X_m(q) \quad (2.8)$$

Because of the appearance of the flow graph of Figure (2.5), this computation is referred as a butterfly computation.

Equations (2.8) suggest a means of reducing the number of complex multiplications by a factor of 2. To see this we note that

$$W_N^{\frac{N}{2}} = e^{-j2\pi \frac{N}{2} \frac{N}{2}} = e^{-j\pi} = -1$$

So the equations (2.8) become

$$X_{m+1}(p) = X_m(p) + W_N^p X_m(q) \quad (2.9)$$

$$X_{m+1}(q) = X_m(p) - W_N^p X_m(q)$$

Equations (2.9) are represented in the flow graph of Figure (2.6).

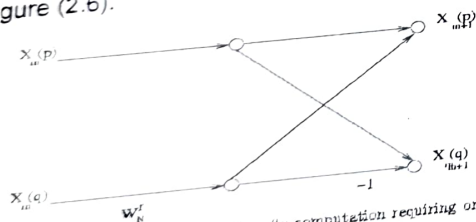


Figure 2.6: Flow graph of simplified butterfly computation requiring only one complex multiplication

Combining the observations in Figures (2.6), (2.5), (2.4) and (2.3), the efficient FFT algorithm in the computational flow graph (2.3), the efficient FFT algorithm is obtained as shown in Figure (2.7). The representation for  $N=8$  is obtained as shown in Figure (2.7). The algorithm requires  $N/2 \log_2 N$  complex multiplications and  $N \log_2 N$

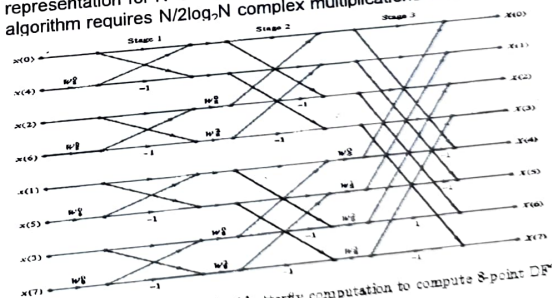


Figure 2.7: Complete flow graph of butterfly computation to compute 8-point DFT



# Decimation-in-Frequency FFT Algorithm

- The decimation-in-time FFT algorithms were all based upon the decomposition of the DFT computation by forming smaller and smaller subsequences.
- Alternatively decimation-in-frequency FFT algorithms are all based upon decomposition of the DFT computation over  $X(k)$  in powers of 2 i.e.  $N = 2^V$

we divide the input sequence into first half and the last half of points so that

$$X(k) = \sum_{n=0}^{N/2-1} x(n)W_N^{nk} + \sum_{n=N/2}^{N-1} x(n)W_N^{nk}, \quad k = 0, 1, \dots, N-1$$

or

$$\begin{aligned} X(k) &= \sum_{n=0}^{N/2-1} x(n)W_N^{nk} + W_N^{N/2 k} \sum_{n=0}^{N/2-1} x(n + \frac{N}{2})W_N^{nk} \\ &= \sum_{n=0}^{N/2-1} [x(n) + (-1)^k x(n + \frac{N}{2})] W_N^{nk} \quad \text{since, } W_N^{N/2 k} = (-1)^k \quad (2.10) \end{aligned}$$

- Separating k-even and k-odd, i.e.  $k=2r$  and  $k=2r+1$ , representing the even-numbered points and the odd-numbered points, respectively, so that

$$X(2r) = \sum_{n=0}^{N/2-1} [x(n) + x(n + \frac{N}{2})] W_{N/2}^{nr}, \quad r = 0, 1, \dots, \frac{N}{2} - 1 \quad (2.11)$$

$$X(2r+1) = \sum_{n=0}^{N/2-1} [x(n) - x(n + \frac{N}{2})] W_{N/2}^{nr}, \quad r = 0, 1, \dots, \frac{N}{2} - 1 \quad (2.12)$$

Thus on the basis of Equations (2.11) and (2.12) with

$$g(n) = x(n) + x(n + \frac{N}{2})$$

and

$$h(n) = x(n) - x(n + \frac{N}{2})$$

The DFT can be computed by first forming the sequences  $g(n)$  and  $h(n)$ , then computing  $h(n)W_{N/2}^n$ , and finally computing the  $N/2$ -point DFTs of these two sequences to obtain the even-numbered output points and odd-numbered output points, respectively.

The procedure suggested by Eqs. (2.11) and (2.12) is illustrated through signal flow graph for the case of 8-point DFT in Figure (2.8).

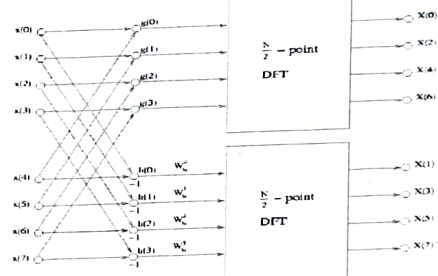


Figure 2.8: Flow graph of the decimation-in-frequency decomposition of an  $N$ -point computation into two  $\frac{N}{2}$ -point DFT computations, for  $N = 8$

Proceeding in a manner similar to that followed in deriving the decimation-in-time algorithm, the final signal flow graph for computation is shown in Figure (2.9).

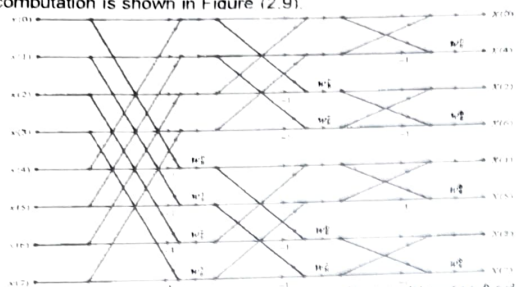


Figure 2.9: Flow graph of the decimation-in-frequency decomposition of an 8-point DFT computation.

- By counting the arithmetic operations in Figure (2.9), and generalizing, we see that the computation of Figure (2.9) requires  $N/2 \log_2 N$  complex multiplications and  $N \log_2 N$  complex additions. Thus the total computation is the same for decimation-in-frequency and decimation-in-time algorithms.
- Similar to decimation-in-time algorithm the computational flow graph shown in Figure (2.9) will indicate the in-place computation capability of decimation-in-frequency algorithm.
- Figure (2.9) is the transpose of Figure (2.7).

## Decimation-In-Time FFT Algorithms

- Makes use of both symmetry and periodicity
- Consider special case of  $N$  an integer power of 2
- Separate  $x[n]$  into two sequences of length  $N/2$ 
  - Even indexed samples in the first sequence
  - Odd indexed samples in the other sequence

$$X[k] = \sum_{n=0}^{N-1} x[n] e^{-j2\pi kn/N} = \sum_{n \text{ even}}^{N/2-1} x[n] e^{-j2\pi kn/N} + \sum_{n \text{ odd}}^{N/2-1} x[n] e^{-j2\pi kn/N}$$

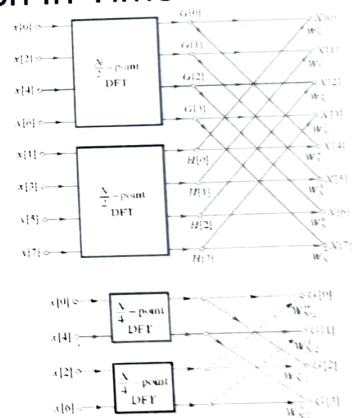
- Substitute variables  $n=2r$  for  $n$  even and  $n=2r+1$  for odd

$$\begin{aligned} X[k] &= \sum_{r=0}^{N/2-1} x[2r] W_N^{2rk} + \sum_{r=0}^{N/2-1} x[2r+1] W_N^{2r(k+1/2)} \\ &= \sum_{r=0}^{N/2-1} x[2r] W_{N/2}^{rk} + W_N^k \sum_{r=0}^{N/2-1} x[2r+1] W_{N/2}^{rk} \\ &= G[k] + W_N^k H[k] \end{aligned}$$

- $G[k]$  and  $H[k]$  are the  $N/2$ -point DFTs of each subsequence

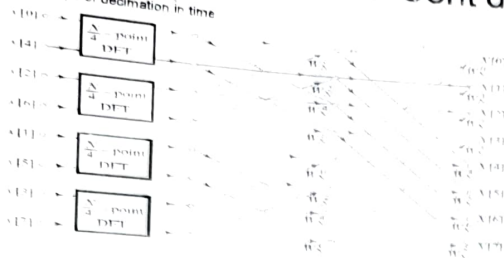
## Decimation In Time

- 8-point DFT example using decimation-in-time
- Two  $N/2$ -point DFTs
  - $2(N/2)^2$  complex multiplications
  - $2(N/2)^2$  complex additions
- Combining the DFT outputs
  - $N$  complex multiplications
  - $N$  complex additions
- Total complexity
  - $N^2/2 + N$  complex multiplications
  - $N^2/2 + N$  complex additions
  - More efficient than direct DFT
- Repeat same process
  - Divide  $N/2$ -point DFTs into
  - Two  $N/4$ -point DFTs
  - Combine outputs



## Decimation In Time Cont'd

- After two steps of decimation in time

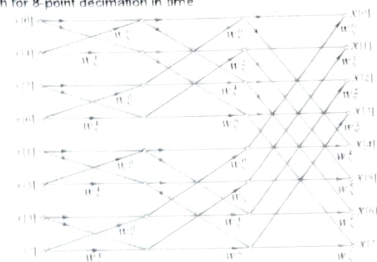


- Repeat until we're left with two-point DFT's



## Decimation-In-Time FFT Algorithm

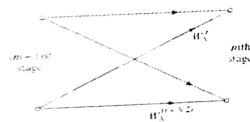
- Final flow graph for 8-point decimation in time



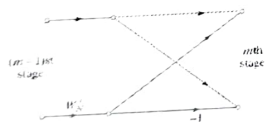
- Complexity
  - $N \log_2 N$  complex multiplications and additions

## Butterfly Computation

- Flow graph constitutes of butterflies



- We can implement each butterfly with one multiplication



- Final complexity for decimation-in-time FFT
  - $(N/2) \log_2 N$  complex multiplications and additions

## In-Place Computation

- Decimation-in-time flow graphs require two sets of registers
  - Input and output for each stage
- Note the arrangement of the input indices
  - Bit reversed indexing

$$\begin{aligned}
 X_0[0] &= x[0] \leftrightarrow X_0[000] = x[000] \\
 X_0[1] &= x[4] \leftrightarrow X_0[001] = x[100] \\
 X_0[2] &= x[2] \leftrightarrow X_0[010] = x[010] \\
 X_0[3] &= x[6] \leftrightarrow X_0[011] = x[110] \\
 X_0[4] &= x[1] \leftrightarrow X_0[100] = x[001] \\
 X_0[5] &= x[5] \leftrightarrow X_0[101] = x[101] \\
 X_0[6] &= x[3] \leftrightarrow X_0[110] = x[011] \\
 X_0[7] &= x[7] \leftrightarrow X_0[111] = x[111]
 \end{aligned}$$

## Decimation-In-Frequency FFT Algorithm

- The DFT equation

$$X[k] = \sum_{n=0}^{N-1} x[n] W_N^{nk}$$

- Split the DFT equation into even and odd frequency indexes

$$X[2r] = \sum_{n=0}^{N/2-1} x[n] W_N^{n2r} = \sum_{n=0}^{N/2-1} x[n] W_N^{n2r} + \sum_{n=N/2}^{N-1} x[n] W_N^{n2r}$$

- Substitute variables to get

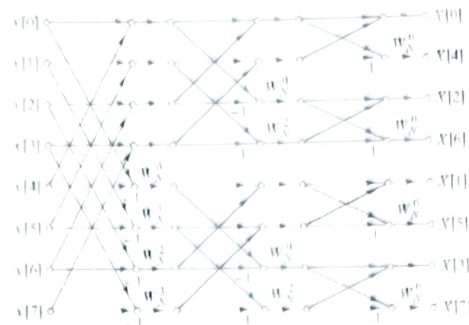
$$X[2r] = \sum_{n=0}^{N/2-1} x[n] W_N^{n2r} + \sum_{n=0}^{N/2-1} x[n + N/2] W_N^{(n+N/2)2r} = \sum_{n=0}^{N/2-1} (x[n] + x[n + N/2] W_N^{n2r})$$

- Similarly for odd-numbered frequencies

$$X[2r+1] = \sum_{n=0}^{N/2-1} (x[n] - x[n + N/2] W_N^{n2r})$$

## Decimation-In-Frequency FFT Algorithm

- Final flow graph for 8 point decimation in frequency



## FFT vs. DFT

- The FFT is simply an algorithm for efficiently calculating the DFT
- Computational efficiency of an N-Point FFT:

DFT:	$N^2$	Complex Multiplications
FFT:	$(N/2) \log_2(N)$	Complex Multiplications

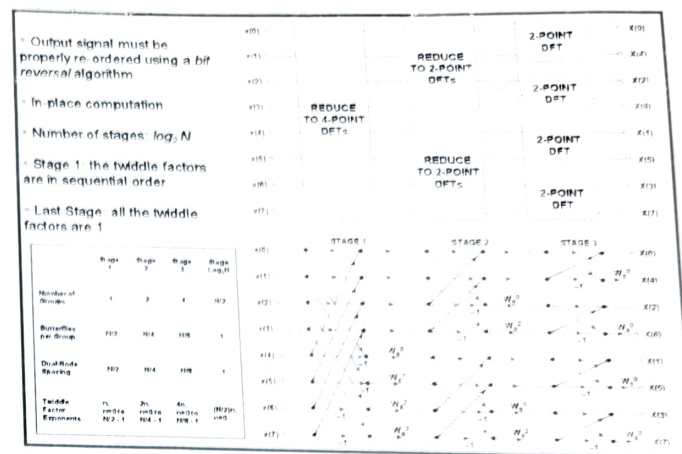
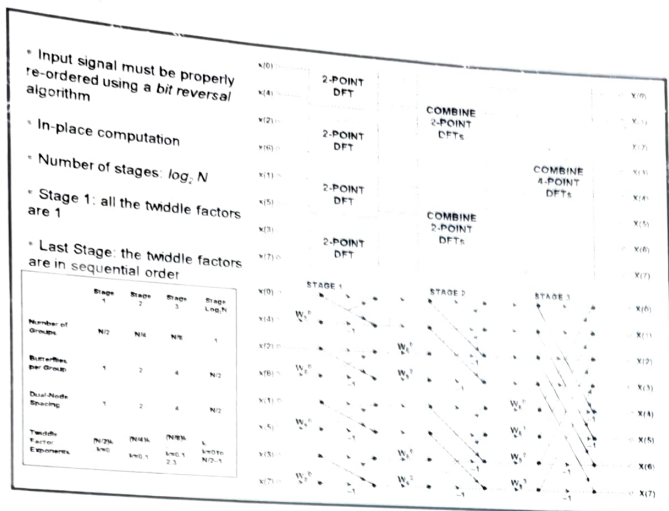
N	DFT Multiplications	FFT Multiplications	FFT Efficiency
256	65,536	1,024	64 : 1
512	262,144	2,304	114 : 1
1,024	1,048,576	5,120	205 : 1
2,048	4,184,384	11,264	372 : 1
4,096	16,777,216	24,576	683 : 1

## Bit Reversal

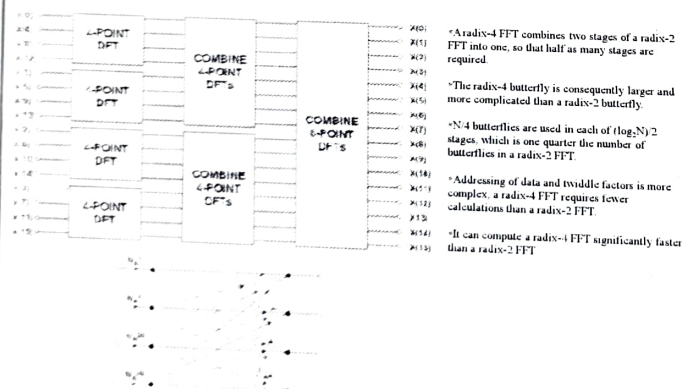
Decimal Number :	0	1	2	3	4	5	6	7
Binary Equivalent :	000	001	010	011	100	101	110	111
Bit-Reversed Binary :	000	100	010	110	001	101	011	111
Decimal Equivalent :	0	4	2	6	1	5	3	7

- The *bit reversal* algorithm used to perform the re-ordering of signals.
- The decimal index,  $n$ , is converted to its binary equivalent.
- The binary bits are then placed in reverse order, and converted back to a decimal number.
- Bit reversing is often performed in DSP hardware in the data address generator (DAG).





## Radix-4 Decimation-In-Time FFT Algorithm



## Inverse Discrete Fourier Transform (IDFT)

The inverse discrete Fourier transform (IDFT) is given by

$$x(n) = \frac{1}{N} \sum_{k=0}^{N-1} X(k) W_N^{-kn}, \quad n = 0, 1, \dots, N-1 \quad (2.13)$$

which is structurally similar to DFT,

$$X(k) = \sum_{n=0}^{N-1} x(n) W_N^{kn}, \quad k = 0, 1, \dots, N-1 \quad (2.14)$$

The change we notice is in the multiplication factor  $1/N$  and replacement of  $W_N^{kn}$  by  $W_N^{-kn}$ , and the interchange of signals  $x(n)$  and  $X(k)$  in the expressions and the index for summation.

- Thus in Figure (2.7) and (2.9), if we exercise the above changes, the changed signal flow graphs will become algorithms for IDFT and referred as IFFT algorithms.

## Example

- Using decimation-in-time FFT algorithm compute DFT of the sequence

$$\{-1 -1 -1 -1 1 1 1 1\}$$

- Solution: Twiddle factors are

$$W_N^0 = e^{-j2\pi \cdot 0/N} = e^{-j0} = 1.000 - j0.000$$

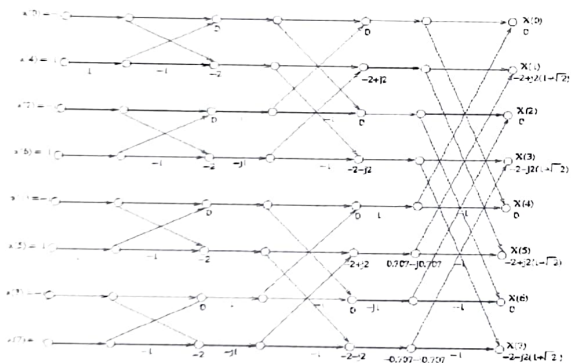
$$W_N^1 = 1$$

$$W_N^2 = 0.707 - j0.707$$

$$W_N^3 = -j1$$

$$W_N^4 = -0.707 - j0.707$$

Solution and signal flow graph of the example



Thank You

BVVS

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Department of Electronics and Communication Engineering

## Fast Fourier Transform (FFT) Algorithms

Dr. K. Shridhar

## Relation to the z-transform

$$x(n) = \begin{cases} \text{Non zero,} & 0 \leq n \leq N-1 \\ 0, & \text{elsewhere} \end{cases} \quad X(z) = \sum_{n=0}^{N-1} x(n)z^{-n}$$

$$x(n) = \begin{cases} \tilde{x}(n), & 0 \leq n \leq N-1 \\ 0, & \text{elsewhere} \end{cases} \quad \tilde{X}(k) = \sum_{n=0}^{N-1} x(n) \left[ e^{j \frac{2\pi}{N} k} \right]^n$$

$$\tilde{X}(k) = X(z) \Big|_{z=e^{j \frac{2\pi}{N} k}}$$

The DFT,  $X(k)$  represents  $N$  equally spaced samples of z-transform  $X(z)$ , on the circumference of unit circle.

## Relation to the DTFT

$$X(e^{j\omega}) = \sum_{n=0}^{N-1} x(n)e^{-j\omega n} = \sum_{n=0}^{N-1} \tilde{x}(n)e^{-j\omega n}$$

$$\tilde{X}(k) = X(e^{j\omega}) \Big|_{\omega=\frac{2\pi}{N}k}$$

$$\text{Let } \omega_1 = \frac{2\pi}{N}, \text{ and } \omega_k = \frac{2\pi}{N}k = k\omega_1$$

$$X(k) = X(e^{j\omega_k}) = X(e^{jk\omega_1})$$

DFT is obtained by uniformly sampling the DTFT at  $\omega_1$  intervals

The interval  $\omega_1$  is the *sampling interval* in the frequency domain. It is called *frequency resolution* because it tells us the minimum frequency quantum of information which we can have about the signal. Smaller the value better the resolution.

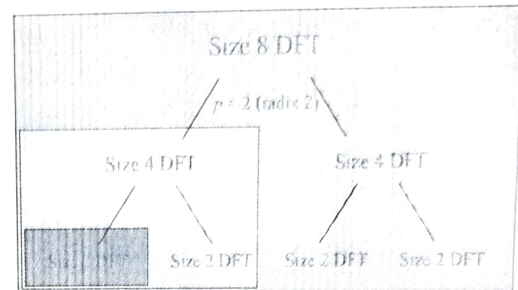
## Comments

- When we sample  $X(z)$  on the circumference of unit circle, we obtain a periodic sequence in the time domain.
- This sequence is a linear combination of the original  $x(n)$  and its infinite replicas, each shifted by multiples of  $N$  or  $-N$  samples.
- If  $x(n)=0$  for  $n < 0$  and  $n \geq N$ , then there will be no overlap or aliasing in the time domain.

## Important Points

- Zero-padding is an operation in which extra zeros are appended to the original sequence at the end of the signal. The resulting longer DFT provides closely spaced samples of the discrete Fourier transform of the original sequence.
- The zero-padding gives us a high-density spectrum and provides a better display of the spectrum. But it does not provide high-resolution spectrum because no new information is added to the signal; only additional zeros are inserted in the data.
- To get high-resolution spectrum, we need more data.
- More the data, more the information

## Divide & Conquer Method



## Important Computational Requirements

- Total number of computations should be linear function rather than quadratic function of N.
- Most of the computations can be eliminated using the symmetry and periodicity properties of Twiddle Factors

$$W_N^{kn} = W_N^{k(n+N)} = W_N^{(k+N)n}$$

$$W_N^{kn+N/2} = -W_N^{kn}$$

Decimation-in-time FFT Algorithm: DIT-FFT Algorithm

Decimation-in-frequency FFT Algorithm: DIF-FFT Algorithm

## Radix-2 FFT Algorithms (DIT-FFT Algorithm)

- Let  $N=2^v$ ; then we choose  $M=2$  and  $L=N/2$  and divide  $x(n)$  into two  $N/2$ -point sequence.
- Above step is repeated. At each stage the sequences are decimated and the smaller DFTs are combined. This decimation ends after  $v$  stages.
- The resulting procedure is called Decimation-in-Time FFT (DIT-FFT) algorithm, for which the total number of complex multiplications is:  $C_N = Nv = N \log_2 N$ .
- Using additional symmetries:  $C_N = Nv = N/2 \log_2 N$



## Radix-2 FFT Algorithms (DIT-FFT Algorithm)

- Choose  $L=2$ ,  $M=N/2$  and follow the steps of DIT-FFT algorithm.
- Its signal flow graph is transposed structure of the DIT-FFT structure.
- Its computational complexity is also equal to  $C_N = NV = N/2 \cdot \log_2 N$

## Merits of Radix-2 FFT Algorithms

- To reduce computational complexity and increase computational efficiency, it is necessary to decompose  $N$  point DFT computation into successively smaller ( $N/2$ ,  $N/4$ ,  $N/8$ ,  $N/16$ , ..... ) DFT computations. In this process we have to exploit both symmetry and periodicity property of complex exponentials

$$W_N^{kn} = W_N^{k(n+N)} = W_N^{(k+N)n}$$

$$W_N^{kn+N/2} = -W_N^{kn}$$

## Decimation-in-time FFT Algorithm

- The principle of decimation-in-time is most conveniently illustrated by considering the special case of Let  $N = \text{An integer power of } 2$ :  $N=2^v$
- Since  $N$  is an even integer, we can consider computing  $X(k)$  by separating  $x(n)$  into two  $N/2$ -point sequences consisting of the even-indexed points and odd-indexed points in  $x(n)$ . Mathematically  $X(k)$  is given by

$$X(k) = \sum_{n=0}^{N-1} x(n) W_N^{kn}, \quad k=0, \dots, N-1 \quad (2.3)$$

- Separating  $x(n)$  into even-numbered and odd-numbered points, we get new expression for  $X(k)$

$$X(k) = \sum_{n \text{ even}} x(n) W_N^{kn} + \sum_{n \text{ odd}} x(n) W_N^{kn}$$

Now substitute  $n=2r$  for an even and  $n=2r+1$  for odd indexed samples

$$X(k) = \sum_{r=0}^{N/2-1} x(2r) W_N^{k(2r)} + \sum_{r=0}^{N/2-1} x(2r+1) W_N^{k(2r+1)}$$

$$= \sum_{r=0}^{N/2-1} x(2r) (W_N^2)^{rk} + W_N^k \sum_{r=0}^{N/2-1} x(2r+1) (W_N^2)^{rk} \quad (2.4)$$

But we know that  $W_N^2 = W_{N/2}$

As a result previous equation (2.4) can be written as

$$X(k) = \sum_{n=0}^{N/2-1} x(2n)W_N^{kn} + W_N^k \sum_{n=0}^{N/2-1} x(2n+1)W_N^{kn} \\ = G(k) + W_N^k H(k), \quad k = 0, 1, \dots, N-1 \quad (2.5)$$

Each of the sums in equation (2.5) is recognized as an  $N/2$ -point DFT. Each of sums need only be computed for  $k = 0$  to  $N/2$  to give  $G(k)$  and  $H(k)$ . Since each  $G(k)$  and  $H(k)$  are periodic in  $k$  with period  $N/2$  further they can be decomposed into two  $N/4$  point DFTs. This process should be continued till we reach at two point DFTs.

The computational flow or the signal flow in computing  $X(k)$  according to Eq. (2.5) for an 8-point sequence, i.e.  $N=8$  is shown in Figure below.

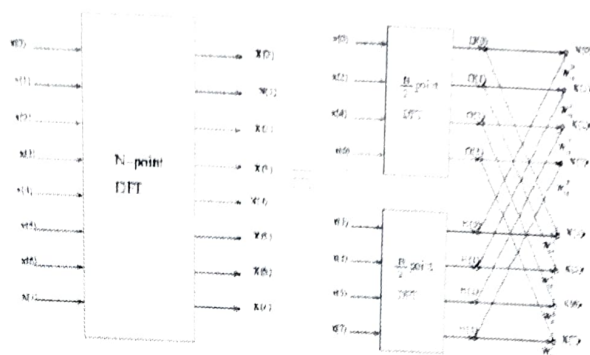


Figure 2.1: Flow graph of the decimation-in-time decomposition of an  $N$  ( $N=8$ ) point DFT into 4-point DFT.

- Further  $G(k)$  and  $H(k)$  in equation (2.5) can be computed as indicated in the next slide

$$G(k) = \sum_{n=0}^{N/2-1} g(n)W_{N/2}^{kn} \\ = \sum_{n=0}^{N/4-1} g(2n)W_{N/2}^{2kn} + W_{N/2}^{kN/4} \sum_{n=0}^{N/4-1} g(2n+1)W_{N/2}^{2kn} \\ = \sum_{n=0}^{N/4-1} g(2n)W_{N/4}^{kn} + W_{N/4}^{kN/2} \sum_{n=0}^{N/4-1} g(2n+1)W_{N/4}^{kn} \quad (2.6)$$

On the same lines

$$H(k) = \sum_{n=0}^{N/2-1} h(n)W_{N/2}^{kn} + W_{N/2}^{kN/4} \sum_{n=0}^{N/4-1} h(2n+1)W_{N/2}^{2kn} \quad (2.7)$$

Where  $g(r)=x(2r)$  and  $h(r)=x(2r+1)$

If the 4-point DFTs in Figure (2.1) are computed according to equations (2.6) and (2.7), then those computations would be carried out as indicated in Figure (2.2) drawn below

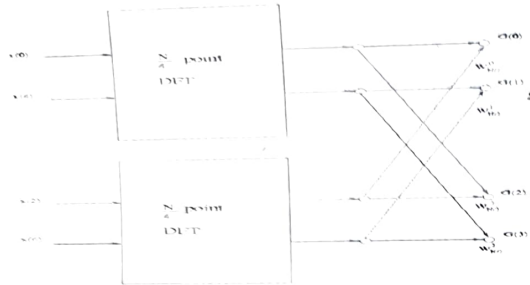


Figure 2.2: Flow graph of decimation-in-time decomposition of an  $N$ -point DFT into  $N/2$ -point DFT ( $N=8$ ).

Inserting the computation indicated in Figure (2.2) into the flow graph of Figure (2.1), we obtain the complete flow graph of Figure

(2.3). We have used the fact that  $W_{N/2} = (W_N)^2$ .

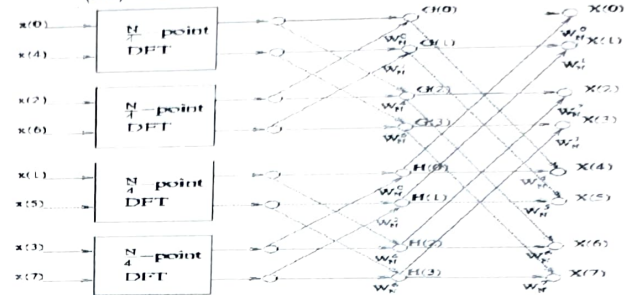


Figure 2.3: Result of substitution of flow graph of Figure (2.1) into flow graph of Figure (2.2).

## In-place Computations

- In view of Figure (2.4), the Figure (2.3) gives the complete computational flow graph for the  $N$ -point computation of DFT of  $N$ -point sequence, for  $N=8$ .
- An interesting by-product of this derivation is that, this flow graph, in addition to describing efficient procedure for computing the DFT, also suggests a useful way of storing the original data and storing the results of the computation in the intermediate arrays.

For  $N=8$ ,  $N/4$ -point DFT becomes 2-point DFT. The 2-point DFT of, for example,  $x(0)$  and  $x(4)$  is depicted in

Figure (2.4).

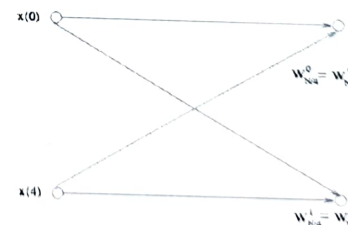


Figure 2.4: Flow graph of a two-point DFT (for  $N=8$ )

We shall denote the sequence of complex numbers resulting from the  $m^{\text{th}}$  stage of computation as  $X_m(l)$ , where  $l=0,1,\dots,N-1$ , and  $m=1,2,\dots$ , forming an input to the  $(m+1)^{\text{st}}$  stage and producing an output  $X_{m+1}(l)$  as the output from the  $(m+1)^{\text{st}}$  stage of computations, it can be seen that the basic computation in flow graph of Figure

(2.3)

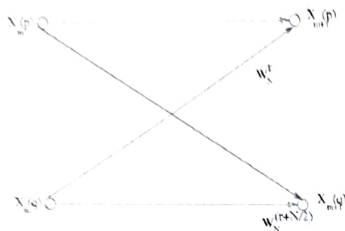


Figure 2.5. Flow graph of basic butterfly computation in Figure (2.3)

The equations represented by this flow graph are

$$X_{m+1}(p) = X_m(p) + W_N^p X_m(q)$$

$$X_{m+1}(q) = X_m(p) + W_N^{p+\frac{N}{2}} X_m(q) \quad (2.8)$$

Because of the appearance of the flow graph of Figure (2.5), this computation is referred as a butterfly computation.

Equations (2.8) suggest a means of reducing the number of complex multiplications by a factor of 2. To see this we note that

$$W_N^{\frac{N}{2}} = e^{-j\frac{2\pi}{N} \cdot \frac{N}{2}} = e^{-j\pi} = -1$$

So the equations (2.8) become

$$X_{m+1}(p) = X_m(p) + W_N^p X_m(q)$$

$$X_{m+1}(q) = X_m(p) - W_N^p X_m(q) \quad (2.9)$$

Equations (2.9) are represented in the flow graph of Figure (2.6).

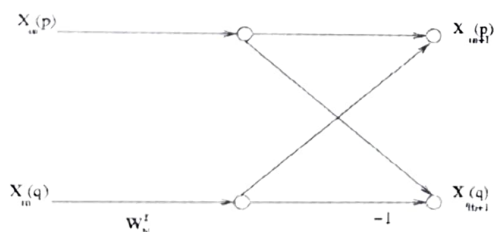


Figure 2.6. Flow graph of simplified butterfly computation requiring only one complex multiplication

Combining the observations in Figures (2.6), (2.5), (2.4) and (2.3), the efficient FFT algorithm in the computational flow graph representation for  $N=8$  is obtained as shown in Figure (2.7). The algorithm requires  $N/2 \log_2 N$  complex multiplications and  $N \log_2$

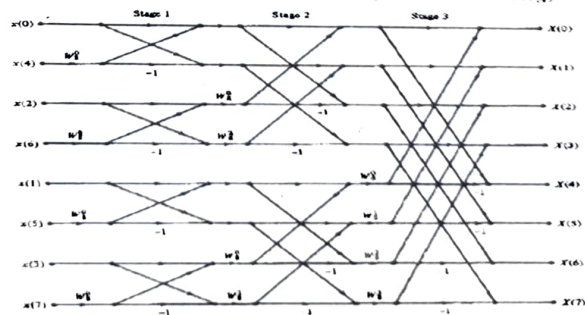


Figure 2.7. Complete flow graph of butterfly computation to compute 8-point DFT



## Decimation-in-Frequency FFT Algorithm

- The decimation-in-time FFT algorithms were all based upon the decomposition of the DFT computation by forming smaller and smaller subsequences.
- Alternatively decimation-in-frequency FFT algorithms are all based upon decomposition of the DFT computation over  $X(k)$  in power of 2 i.e.

$$N = 2^r$$

we divide the input sequence into first half and the last half of points so that

$$X(k) = \sum_{n=0}^{N/2-1} x(n)W_N^{nk} + \sum_{n=N/2}^{N-1} x(n)W_N^{nk}, \quad k = 0, 1, \dots, N-1$$

or

$$X(k) = \sum_{n=0}^{N/2-1} x(n)W_N^{nk} + W_N^{N/2 k} \sum_{n=0}^{N/2-1} x(n + \frac{N}{2})W_N^{nk} \\ = \sum_{n=0}^{N/2-1} [x(n) + (-1)^k x(n + \frac{N}{2})] W_N^{nk} \quad \text{since, } W_N^{N/2 k} = (-1)^k \quad (2.10)$$

- Separating k-even and k-odd, i.e.  $k=2r$  and  $k=2r+1$ , representing the even-numbered points and the odd-numbered points, respectively, so that

$$X(2r) = \sum_{n=0}^{N/2-1} [x(n) + x(n + \frac{N}{2})] W_N^{nr}, \quad r = 0, 1, \dots, \frac{N}{2} - 1 \quad (2.11)$$

$$X(2r+1) = \sum_{n=0}^{N/2-1} [x(n) - x(n + \frac{N}{2})] W_N^{nr} W_N^{N/2 r}, \quad r = 0, 1, \dots, \frac{N}{2} - 1 \quad (2.12)$$

Thus on the basis of Equations (2.11) and (2.12) with

$$g(n) = x(n) + x(n + \frac{N}{2})$$

and

$$h(n) = x(n) - x(n + \frac{N}{2})$$

The DFT can be computed by first forming the sequences  $g(n)$  and  $h(n)$ , then computing  $h(n)W_N^{N/2 r}$ , and finally computing the  $N/2$ -point DFTs of these two sequences to obtain the even-numbered output points and odd-numbered output points, respectively.

The procedure suggested by Eqs. (2.11) and (2.12) is illustrated through signal flow graph for the case of 8-point DFT in Figure (2.8).

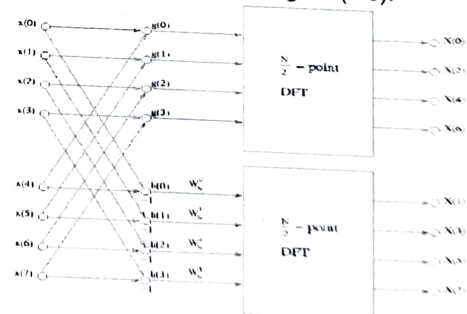


Figure 2.8: Flow graph of the decimation-in-frequency decomposition of an  $N$ -point computation into two  $\frac{N}{2}$ -point DFT computations, for  $N = 8$

proceeding in a manner similar to that followed in deriving the decimation-in-time algorithm, the final signal flow graph for computation is shown in Figure (2.9).

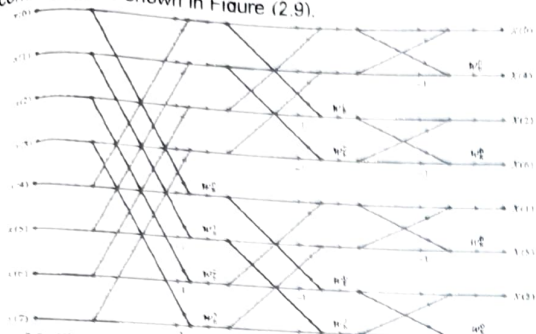


Figure 2.9: Flow graph of the decimation-in-frequency decomposition of an 8-point DFT computation.

- By counting the arithmetic operations in Figure (2.9), and generalizing, we see that the computation of Figure (2.9) requires  $N/2 \log_2 N$  complex multiplications and  $N \log_2 N$  complex additions. Thus the total computation is the same for decimation-in-frequency and decimation-in-time algorithms.
- Similar to decimation-in-time algorithm the computational flow graph shown in Figure (2.9) will indicate the in-place computation capability of decimation-in-frequency algorithm.
- Figure (2.9) is the transpose of Figure (2.7).

## Decimation-In-Time FFT Algorithms

- Makes use of both symmetry and periodicity
- Consider special case of  $N$  an integer power of 2
- Separate  $x[n]$  into two sequence of length  $N/2$ 
  - Even indexed samples in the first sequence
  - Odd indexed samples in the other sequence

$$X[k] = \sum_{n=0}^{N-1} x[n] e^{-j2\pi/Nkn} = \sum_{n \text{ even}} x[n] e^{-j2\pi/Nkn} + \sum_{n \text{ odd}} x[n] e^{-j2\pi/Nkn}$$

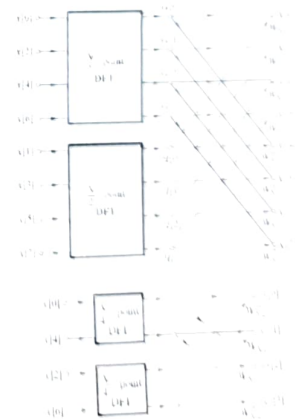
- Substitute variables  $n=2r$  for  $r$  even and  $n=2r+1$  for odd

$$\begin{aligned} X[k] &= \sum_{r=0}^{N/2-1} x[2r] W_N^{2rk} + \sum_{r=0}^{N/2-1} x[2r+1] W_N^{2(r+1)k} \\ &= \sum_{r=0}^{N/2-1} x[2r] W_{N/2}^{rk} + W_N^k \sum_{r=0}^{N/2-1} x[2r+1] W_{N/2}^{rk} \\ &= G[k] + W_N^k H[k] \end{aligned}$$

- $G[k]$  and  $H[k]$  are the  $N/2$ -point DFTs of each subsequence

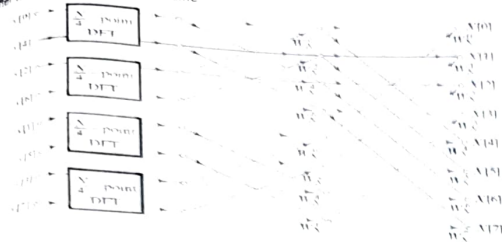
## Decimation In Time

- 8-point DFT example using decimation-in-time
- Two  $N/2$ -point DFTs
  - $2(N/2)^2$  complex multiplications
  - $2(N/2)$  complex additions
- Combining the DFT outputs
  - $N$  complex multiplications
  - $N$  complex additions
- Total complexity
  - $N^2/2 + N$  complex multiplications
  - $N^2/2 + N$  complex additions
  - More efficient than direct DFT
- Repeat same process
  - Divide  $N/2$  point DFTs into
  - Two  $N/4$  point DFTs
  - Combine outputs

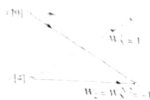


## Decimation In Time Cont'd

After two steps of decimation in time

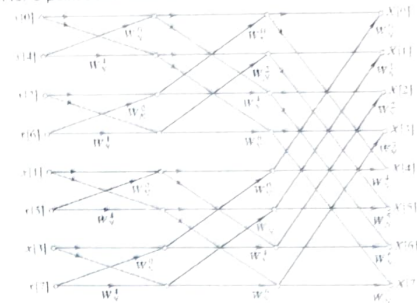


repeat until we're left with two-point DFT's



## Decimation-In-Time FFT Algorithm

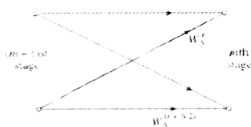
Final flow graph for 8 point decimation in time



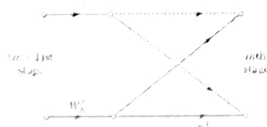
- Complexity:
  - $N \log_2 N$  complex multiplications and additions

## Butterfly Computation

- Flow graph constitutes of butterflies



- We can implement each butterfly with one multiplication



- Final complexity for decimation-in-time FFT
  - $(N/2) \log_2 N$  complex multiplications and additions

## In-Place Computation

- Decimation-in-time flow graphs require two sets of registers
  - Input and output for each stage
- Note the arrangement of the input indices
  - Bit reversed indexing

$$\begin{aligned}
 X_0[0] &= x[0] \leftrightarrow X_0[000] = x[000] \\
 X_0[1] &= x[4] \leftrightarrow X_0[001] = x[100] \\
 X_0[2] &= x[2] \leftrightarrow X_0[010] = x[010] \\
 X_0[3] &= x[6] \leftrightarrow X_0[011] = x[110] \\
 X_0[4] &= x[1] \leftrightarrow X_0[100] = x[001] \\
 X_0[5] &= x[5] \leftrightarrow X_0[101] = x[101] \\
 X_0[6] &= x[3] \leftrightarrow X_0[110] = x[011] \\
 X_0[7] &= x[7] \leftrightarrow X_0[111] = x[111]
 \end{aligned}$$

## Decimation-In-Frequency FFT Algorithm

The DFT equation

$$X[k] = \sum_{n=0}^{N-1} x[n] W_N^{kn}$$

Split the DFT equation into even and odd frequency indexes

$$X[2r] = \sum_{n=0}^{N-1} x[n] W_N^{n2r} = \sum_{n=0}^{N/2-1} x[n] W_N^{n2r} + \sum_{n=N/2}^{N-1} x[n] W_N^{n2r}$$

Separate variables to get

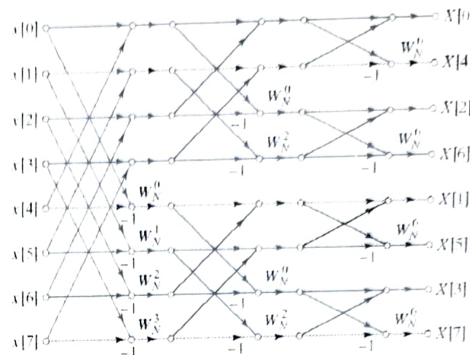
$$X[2r] = \sum_{n=0}^{N/2-1} x[n] W_N^{n2r} + \sum_{n=0}^{N/2-1} x[n + N/2] W_N^{(n+N/2)2r} = \sum_{n=0}^{N/2-1} (x[n] + x[n + N/2] W_N^{nr}) W_N^{nr}$$

Similarly for odd-numbered frequencies

$$X[2r+1] = \sum_{n=0}^{N/2-1} (x[n] - x[n + N/2] W_N^{nr}) W_N^{nr}$$

## Decimation-In-Frequency FFT Algorithm

Final flow graph for 8-point decimation in frequency



## FFT vs. DFT

- The FFT is simply an algorithm for efficiently calculating the DFT

- Computational efficiency of an N-Point FFT:

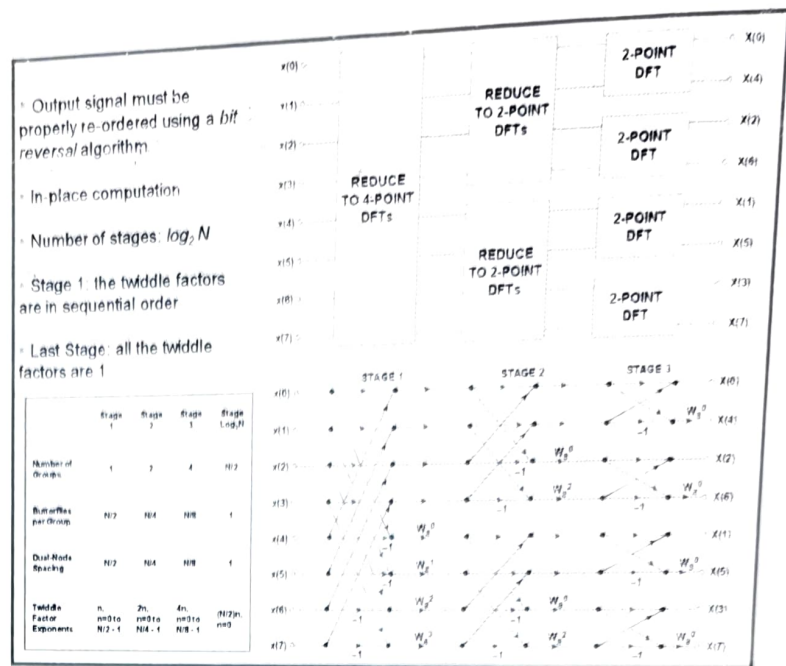
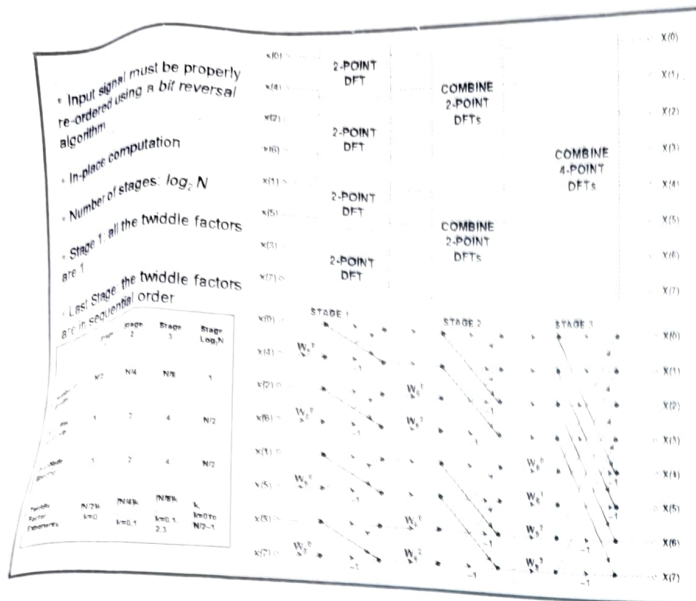
DFT:	$N^2$	Complex Multiplications
FFT:	$(N/2) \log_2(N)$	Complex Multiplications

N	DFT Multiplications	FFT Multiplications	FFT Efficiency
256	65,536	1,024	64 : 1
512	262,144	2,304	114 : 1
1,024	1,048,576	5,120	205 : 1
2,048	4,194,304	11,264	372 : 1
4,096	16,777,216	24,576	683 : 1

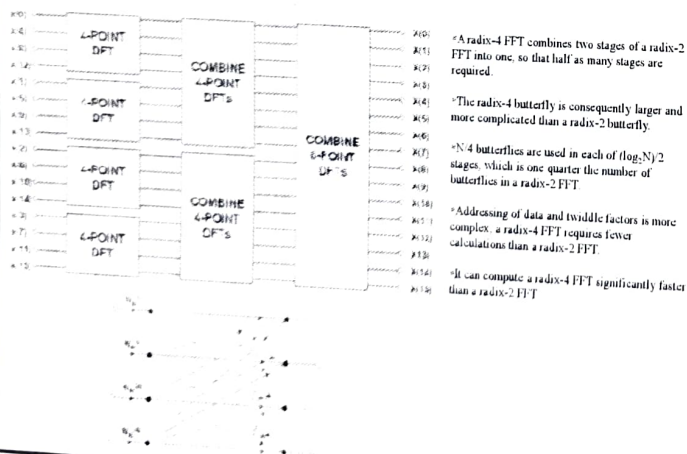
## Bit Reversal

Decimal Number :	0	1	2	3	4	5	6	7
Binary Equivalent :	000	001	010	011	100	101	110	111
Bit-Reversed Binary :	000	100	010	110	001	101	011	111
Decimal Equivalent :	0	4	2	6	1	5	3	7

- The *bit reversal* algorithm used to perform the re-ordering of signals.
- The decimal index,  $n$ , is converted to its binary equivalent.
- The binary bits are then placed in reverse order, and converted back to a decimal number.
- Bit reversing is often performed in DSP hardware in the data address generator (DAG).



## Radix-4 Decimation-In-Time FFT Algorithm



## Inverse Discrete Fourier Transform (IDFT)

The inverse discrete Fourier transform (IDFT) is given by

$$x(n) = \frac{1}{N} \sum_{k=0}^{N-1} X(k) W_N^{-kn}, \quad n = 0, 1, \dots, N-1 \quad (2.13)$$

which is structurally similar to DFT,

$$X(k) = \sum_{n=0}^{N-1} x(n) W_N^{kn}, \quad k = 0, 1, \dots, N-1 \quad (2.14)$$

The change we notice is in the multiplication factor  $1/N$  and replacement of  $W_N^{kn}$  by  $W_N^{-kn}$ , and the interchange of signals  $x(n)$  and  $X(k)$  in the expressions and the index for summation.



- Thus in Figure (2.7) and (2.9), if we exercise the above changes, the changed signal flow graphs will become algorithms for IDFT and referred as IFFT algorithms.

## Example

- Using decimation-in-time FFT algorithm compute DFT of the sequence  $\{-1 -1 -1 -1 1 1 1 1\}$

- Solution: Twiddle factors are

$$W_8^0 = e^{-j2\pi \cdot 0/8} = e^{-j0} = 1.000 - j0.000$$

$$W_8^1 = e^{-j2\pi \cdot 1/8} = 0.707 - j0.707$$

$$W_8^2 = e^{-j2\pi \cdot 2/8} = 0.000 - j1.000$$

$$W_8^3 = e^{-j2\pi \cdot 3/8} = -0.707 - j0.707$$

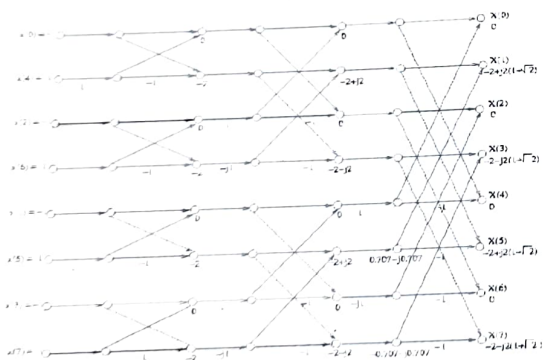
$$W_8^4 = e^{-j2\pi \cdot 4/8} = -1.000 - j0.000$$

$$W_8^5 = e^{-j2\pi \cdot 5/8} = -0.707 + j0.707$$

$$W_8^6 = e^{-j2\pi \cdot 6/8} = 0.707 + j0.707$$

$$W_8^7 = e^{-j2\pi \cdot 7/8} = 0.707 - j0.707$$

## Solution and signal flow graph of the example



Thank You

Samutegi

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# IoT APPLICATIONS

## MCQ ON IOT

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\* Indicates required question

**What is the Internet of Things (IoT)? \***

1 point

- ☐ a) A system that connects all types of devices to the internet
- ☐ b) A technology that allows for remote control of home appliances
- ☐ c) A tool for managing computer networks
- ☐ d) A platform for online shopping

**What is the main purpose of IoT? \***

1 point

- ☐ a) To collect and analyze data from connected devices
- ☐ b) To control home appliances remotely
- ☐ c) To improve online shopping experiences
- ☐ d) To create a virtual reality environment

**. Which of the following is an example of an IoT device? \***

1 point

- ☐ a) A laptop computer
- ☐ b) A fitness tracker
- ☐ c) A television set
- ☐ d) A landline telephone

**Which wireless communication protocol is commonly used in IoT devices? \*** 1 point

- ☐ a) Wi-Fi
- ☐ b) Bluetooth
- ☐ c) NFC
- ☐ d) Infrared

1 point

**What is a sensor in IoT? \***

- ☐ a) A device that converts physical or environmental parameters into digital signals
- ☐ b) A device that connects to the internet and sends data
- ☐ c) A device that provides a graphical user interface
- ☐ d) A device that runs software programs

1 point

**What is a gateway in IoT? \***

- ☐ a) A device that connects IoT devices to the internet
- ☐ b) A device that stores data collected by IoT devices
- ☐ c) A device that analyzes data collected by IoT devices
- ☐ d) A device that provides a user interface for IoT devices

**Which of the following is an example of an IoT application in healthcare? \*** 1 point

- ☐ a) Online shopping for medical supplies
- ☐ b) Remote patient monitoring
- ☐ c) Electronic medical record management
- ☐ d) Telemedicine consultations

**What is a smart home in IoT? \***

- ☐ a) A home that is equipped with IoT devices and systems
- ☐ b) A home that is powered by renewable energy sources
- ☐ c) A home that uses advanced security systems
- ☐ d) A home that is completely automated

**Which of the following is an example of an IoT application in agriculture? \*** 1 point

- ☐ a) Online crop sales platform
- ☐ b) Farm management software
- ☐ c) Weather forecasting app
- ☐ d) Smart irrigation system

**What is edge computing in IoT? \***

1 point

- ☐ a) A process of analyzing data at the source of generation, rather than sending it to a centralized location
- ☐ b) A process of analyzing data on a remote server
- ☐ c) A process of storing data on a local device
- ☐ d) A process of transmitting data over a wired network

1 point

**Which of the following is a disadvantage of IoT? \***

- ☐ a) Increased efficiency and productivity
- ☐ b) Improved decision-making and analytics
- ☐ c) Privacy and security concerns
- ☐ d) Greater connectivity and collaboration

\* 1 point

**Which of the following is an example of an IoT application in transportation?**

- ☐ a) Online ticket booking system
- ☐ b) GPS navigation system
- ☐ c) Traffic management software
- ☐ d) Automotive diagnostic tool

**Which of the following is an example of an IoT application in retail? \***

1 point

- ☐ a) Online advertising platform
- ☐ b) Customer relationship management software
- ☐ c) Inventory management system
- ☐ d) Point of sale system

Which of the following is an example of an IoT application in energy management?

\* 1 point

- ☐ a) Online billing and payment platform
- ☐ b) Smart grid technology
- ☐ c) Energy efficiency audit software
- ☐ d) Renewable energy generation system

1 point

What is the role of cloud computing in IoT? \*

- ☐ a) To store and process data collected by IoT devices
- ☐ b) To provide connectivity between IoT devices
- ☐ c) To analyze data generated by IoT devices
- ☐ d) To manage and control IoT devices

Untitled Question

☐ Option 1

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## Innovation in Teaching Learning Process

### Image Processing \_UEC635N

A rubrics prepared for self evaluation of students for their presentation of assignment: Questioner and responses collected

#### Evaluation of Presentation Assignment 1 (UEC635N)

Please check the level of the responses during presentation.

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## 8. Delivery and Engagement \*

Excellent (5), Good (4), Satisfactory (3), Needs Improvement (2), Inadequate (1)

Mark only one oval per row

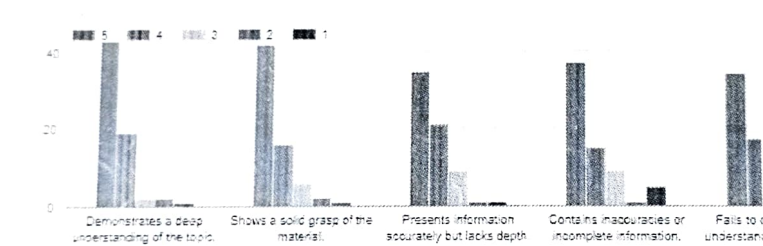
	5	4	3	2	1
Engages the audience with a confident demeanor.	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>
Maintains good eye contact and vocal clarity.	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>
Some engagement but lacks consistent enthusiasm.	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>
Appears nervous or disinterested.	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>
Demonstrates little effort to engage the audience.	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>

## 10. Time Management \*

Excellent (5), Good (4), Satisfactory (3), Needs Improvement (2), Inadequate (1)

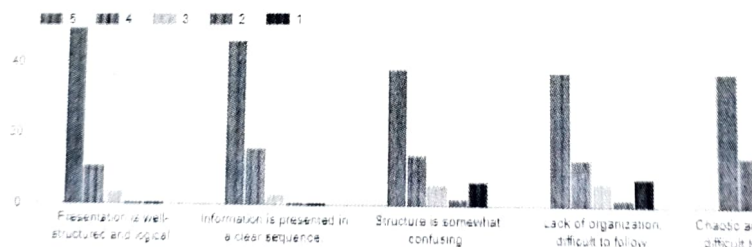
Mark only one oval per row

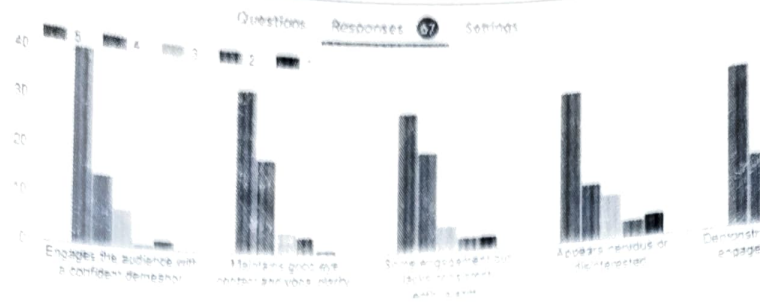
	5	4	3	2	1
Stays within the allocated time limit.	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>
Finishes slightly before or after the time limit.	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>
Goes over or significantly under the time limit.	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>
Exceeds time limit without covering all points.	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>
Presentation is too short and lacks substance.	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>	<input type="radio"/>



Organization and Structure

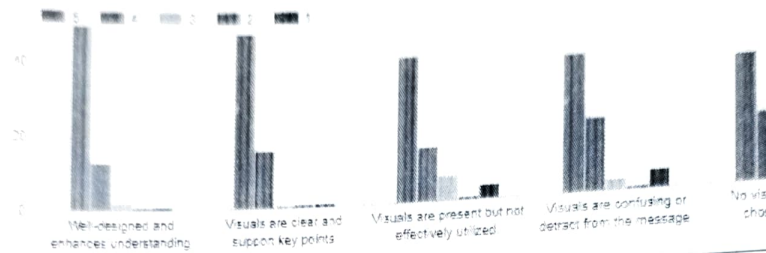
Copy chart





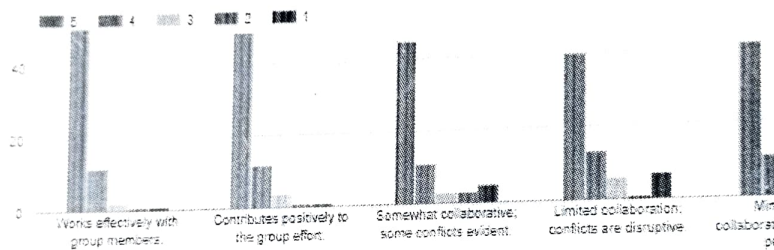
### Visual Aids

☐ Copy chart



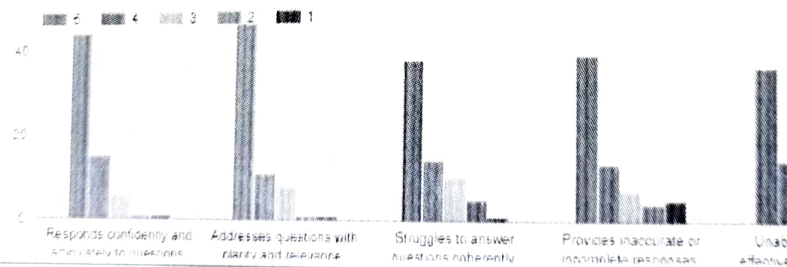
### Collaboration with team members

☐ Copy chart



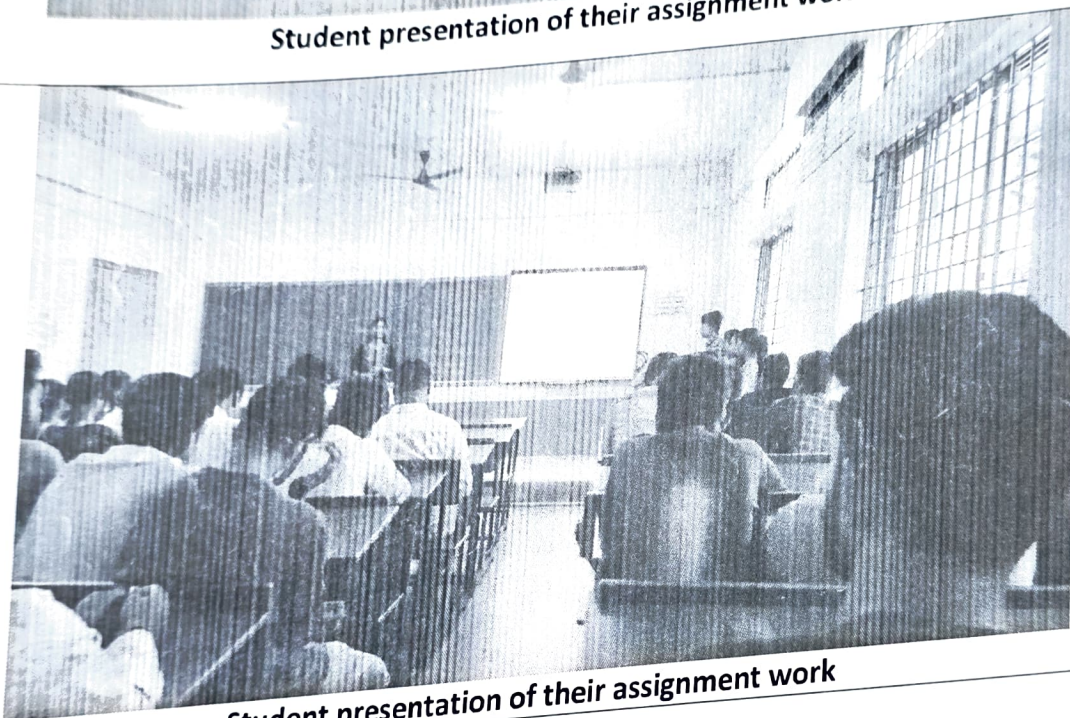
### Q&A Session

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Student presentation of their assignment work



Student presentation of their assignment work

hdi-9  
(Mrs. Vijayalakshmi .S.T)



**Basaveshwar Engineering College, Bagalkot**  
**Department of Electronics and Communication Engineering**  
**(Academic Year 2023-24)**

**Course Project Details**

**Sub:** Computer Networks  
**Div:** A

**Subject Code:** 21UEC603C  
**Staff Incharge:** Dr. Ashok Sutagundar

SL No.	USN	Name of student	Project title
1)	2BA21EC050	Panakaj Biradar	GLBP and Frame Relay Protocols
2)	2BA21EC054	Pooja P Patil	Hotel Management Network Design
3)	2BA21EC055	Pragati Pattanshetti	IOT based Smart Garden using CISCO Packet tracer
4)	2BA21EC057	Prajwal Desai	Fire Alarm System
5)	2BA21EC058	Prajwal H. K.	Temperature sensor data Acquisition
6)	2BA21EC060	Promod Hiremath	Simulation of Distance Vector Routing Protocol
7)	2BA21EC0064	Pratiksha Dodamani	Simulation of IPV4 and Enhance Gateway Routing Protocol
8)	2BA21EC0065	Praveen Hatti	Simulation of Domain System Protocol using Cisco Packet Tracer
9)	2BA21EC016	Anand Hiremani	LCP and PAGP protocols using CISCO Packet tracer
10)	2BA21EC017	A. M. Desai	Hot standby Routing Protocol and NAT protocol translation
11)	2BA21EC020	Arun Budni	Smart Hospital Environment
12)	2BA21EC021	Arundati Bhavikatti	Open VPN and Wire Guard protocol
13)	2BA21EC022	Ashiwini Chouvan	Smart Agriculture using Cisco Packet tracer
14)	2BA21EC023	Avinash	Configuration of DNS server and Client Link Layer Discovery
15)	2BA21EC024	Basavaraj Kohalli	Controlling windows based on CO Level
16)	2BA21EC025	Basavaraj urf Koushik	Solar Powered IoT Devices using Cisco Packet Tracer
17)	2BA21EC029	Chandrashekar G. Hadalagi	Simulation of ICMP protocol using Cisco Packet Tracer
18)	2BA21EC031	Gagan Bhairamatti	Smart Day/Night using Cisco Packet Tracer
19)	2BA21EC032	Ganga Patil	Thief Catcher using IoT system
20)	2BA21EC035	Jyothi Alagodi	Hospital Management System using Cisco Packet Tracer
21)	2BA21EC037	Karthik Kulkarni	Simple Office Networking
22)	2BA21EC038	Kiran Muradi	Smart Garden using IoT
23)	2BA21EC040	Laxmi Nandikolmath	IoT based solar panel
24)	2BA21EC043	Mohmad Rehan Pattankundi	Simulation of CDP and PAT protocol
25)	2BA21EC045	Narayan N Malabasari	Room Automation using Cisco Packet Tracer



26)	2BA21EC048	Nitish	Campus Network Management
27)	2BA21EC049	Pallavi Babu Halki	Smart Home Automation using Cisco Packet tracer

  
 (Dr. Ashok Sutagundar)